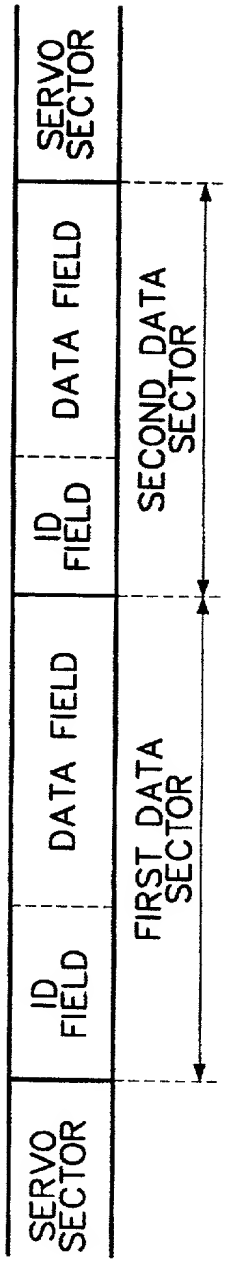

FIG. 1 (Prior Art)

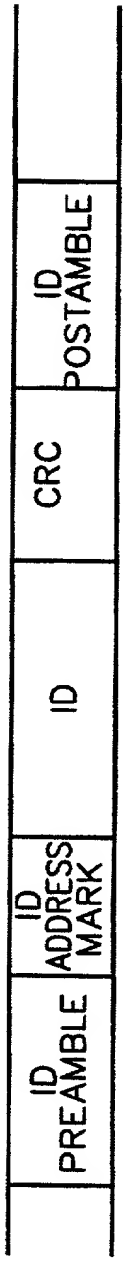

FIG. 2 (Prior Art)

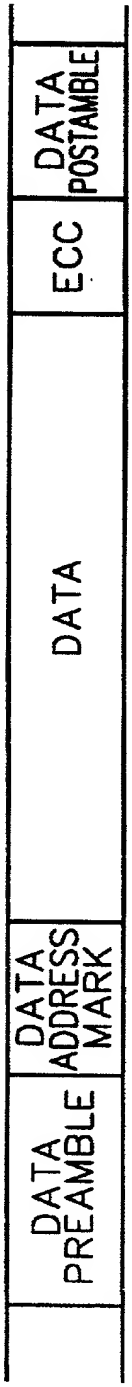
DATA PREAMBLE
DATA ADDRESS MARK
DATA
ECC
DATA POSTAMBLE

FIG. 3

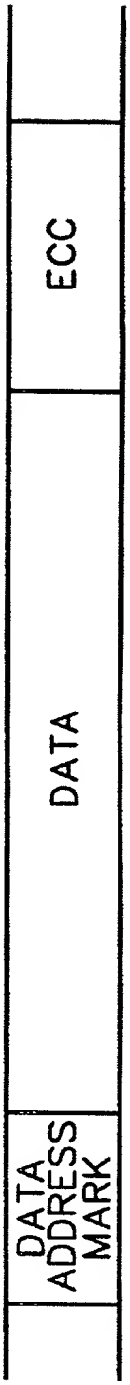
DATA ADDRESS MARK
DATA
ECC

FIG. 4

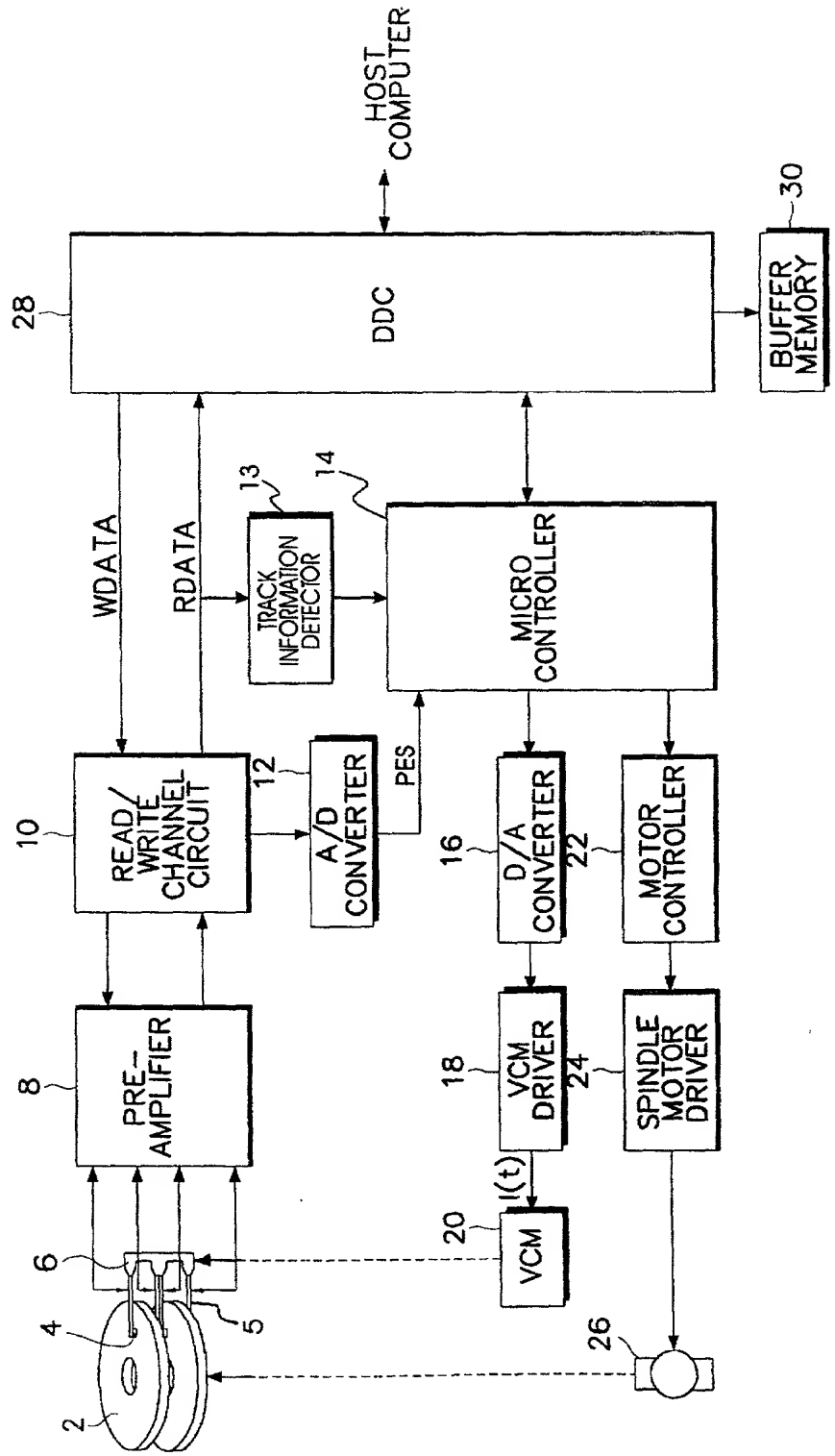
HOST COMPUTER
DDC
28
BUFFER MEMORY
30
WDATA
RDATA
TRACK INFORMATION DETECTOR
13
14
MICRO CONTROLLER
READ/ WRITE CHANNEL CIRCUIT
10
A/D CONVERTER
12
PES
D/A CONVERTER
16
22
MOTOR CONTROLLER
PRE-AMPLIFIER
8
VCM DRIVER
18
24
SPINDLE MOTOR DRIVER
I(t)
VCM
20
6
4
5
2
26

FIG. 5

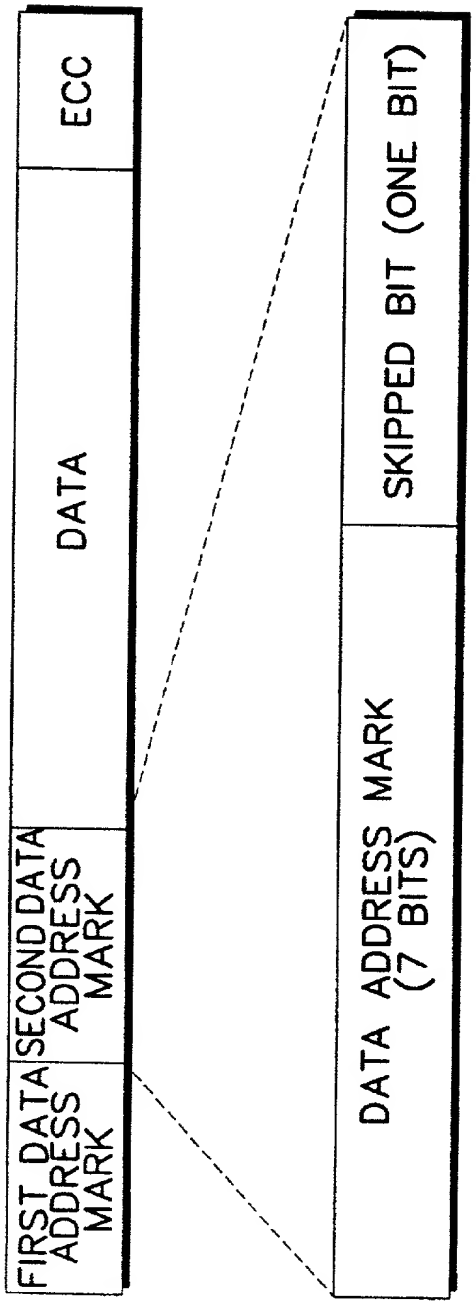
FIRST DATA ADDRESS MARK
SECOND DATA ADDRESS MARK
DATA
ECC
DATA ADDRESS MARK (7 BITS)
SKIPPED BIT (ONE BIT)

FIG. 6